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## \*BIBDATASHEET\*

Bib Data Sheet

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### \*\* CONTINUING DATA \*\*\*\*\*

*None - TD*

### \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*None - TD*

### IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/30/2000

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no				
Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Examiner's Signature <i>JB</i> Initials	DRAWING 7	31	5

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### TITLE

Architecture for circuit connection of a vertical transistor

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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